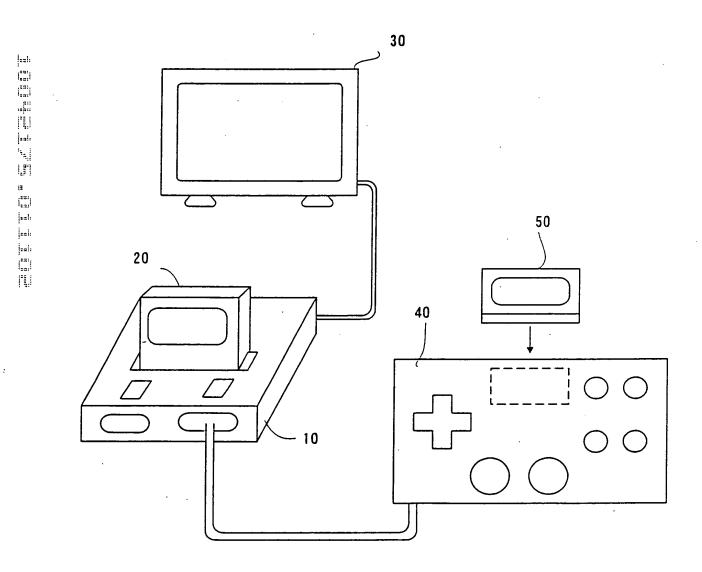


FIG. 1



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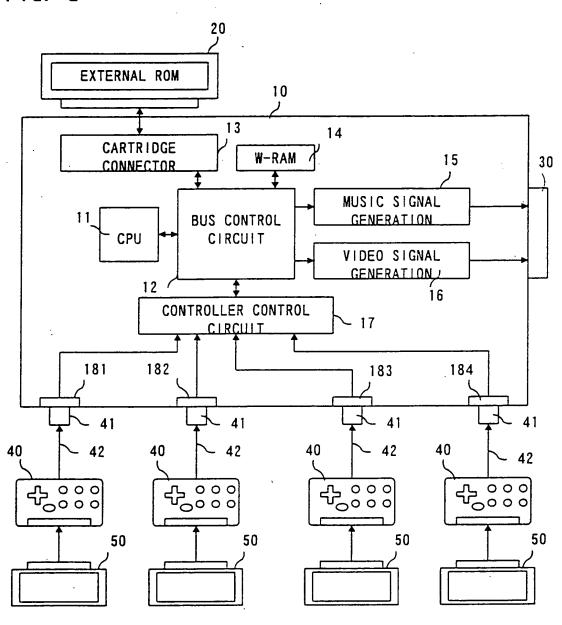
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FIG. 2



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FIG. 3

CPU MEMORY MAP

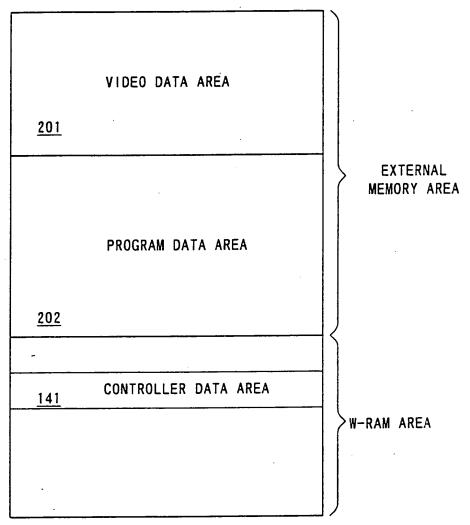


FIG. 4

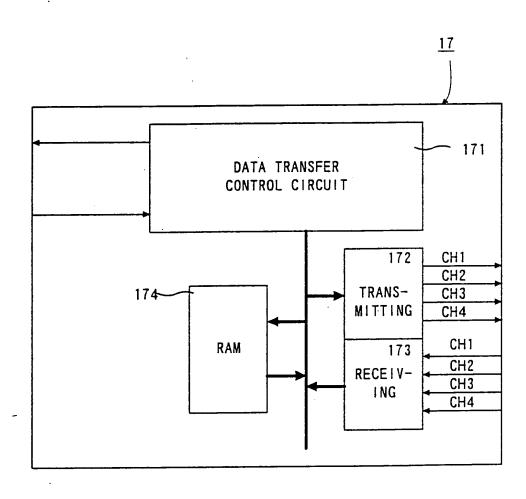
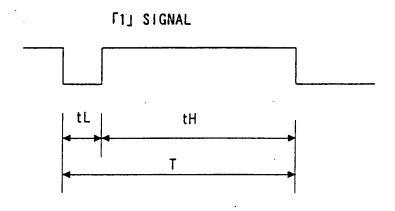


FIG. 5



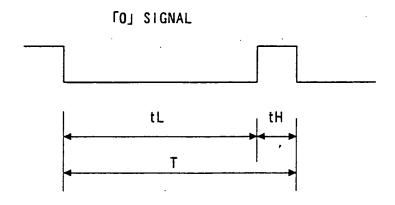


FIG. 6

RAM 174

1CH C	OMMAND	STORAGE	174a	
·		1CH		SMISSION/RECEPTION ATA STORAGE
174b			· · · · · · · · · · · · · · · · · · ·	
2CH C	OMMAND	STORAGE	174c	
		2CH		SMISSION/RECEPTION ATA STORAGE
174d				
3CH C	OMMAND	STORAGE	174e	
		2CH		SMISSION/RECEPTION ATA STORAGE
174f				
4CH C	OMMAND	STORAGE	174g	
		2CH		SMISSION/RECEPTION ATA STORAGE
174h				

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FIG. 7

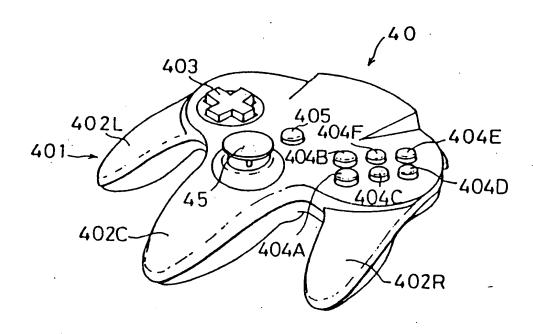
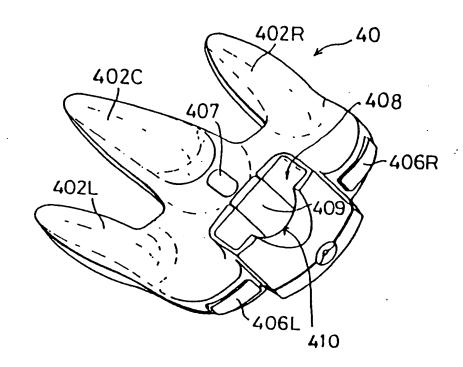
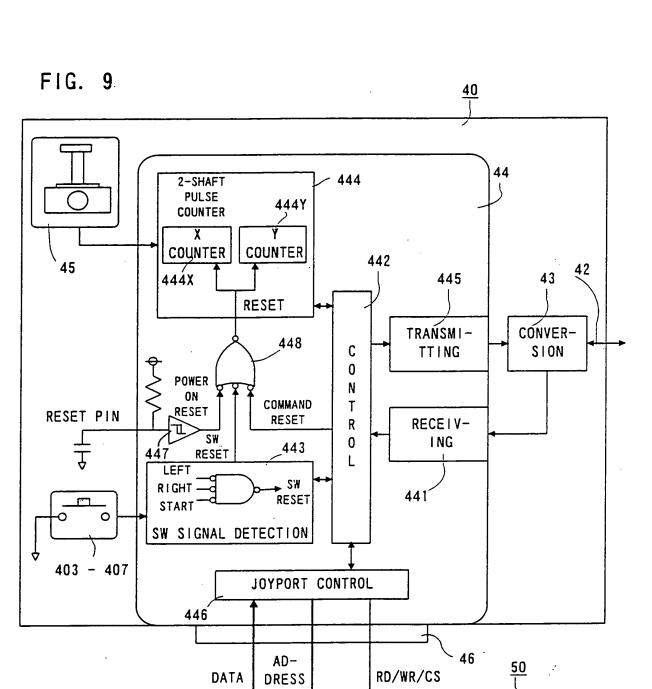


FIG. 8





RAM

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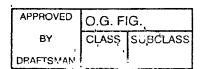


FIG. 10

1 BYTE	В	. A	G	START	1	1	4	
2 BYTE	JSRST	0	L	R	E	D	С	F
3 BYTE	X ORDINATE							
4 BYTE	Y ORDINATE							



COMMAND 0: TRANSMITTING TYPE OF COMMAND RECEPTION: 1 BYTE TRANSMISSION: 3 BYTES

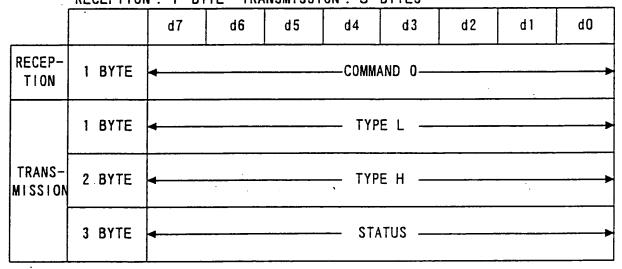
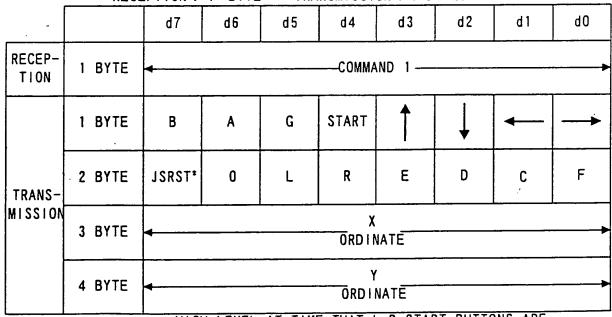


FIG. 12

COMMAND 1: ACCESSING STANDARD CONTROLLER RECEPTION: 1 BYTE TRANSMISSION: 4 BYTES



* HIGH LEVEL AT TIME THAT L, R, START BUTTONS ARE SIMULTANEOUSLY DEPRESSED

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FIG. 13

COMMAND 2: READ-OUT RAM

RECEPTION: 3 BYTES TRANSMISSION: 33 BYTES d7 **d6 d**5 **d4** d3 **d2 d** 1 1 BYTE. --- COMMAND 2 ----RECEP-2 BYTE --- ADDRESS H ----TION 3 BYTE ----- ADDRESS L ----------- ADDRESS CRC -1 BYTE ---- DATA 0 ---2 BYTE —— DATA 1 — TRANS-MISSION 32 BYTE —— DATA 31 ———— —— DATA CRC — 33 BYTE



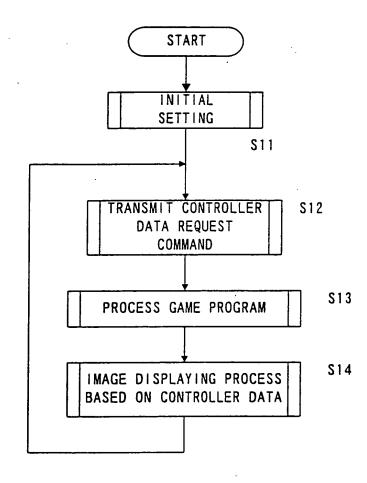
FIG. 14

COMMAND 3: WRITING INTO RAM
ECEPTION: 35 BYTES TRANSMISSION: 1 B

TRANSMISSION: 1 BYTE RECEPTION: 35 BYTES d1 d0 **d4** d3 d2 đ5 d7 d6 _____ COMMAND 3 ____ 1 BYTE ——— ADDRESS H ——— 2 BYTE ———— ADDRESS CRC — → ADDRESS L → → 3 BYTE RECEP-_____ DATA 0 ____ 4 BYTE TION ____ DATA 1 ____ 5 BYTE _____ DATA 31 _____ 35 BYTE ► TRANS-1 BYTE _____ DATA CRC -MISSION

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FIG. 15



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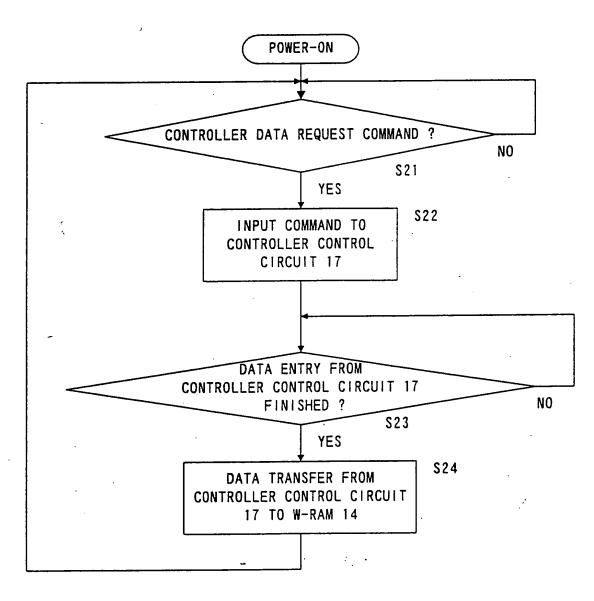
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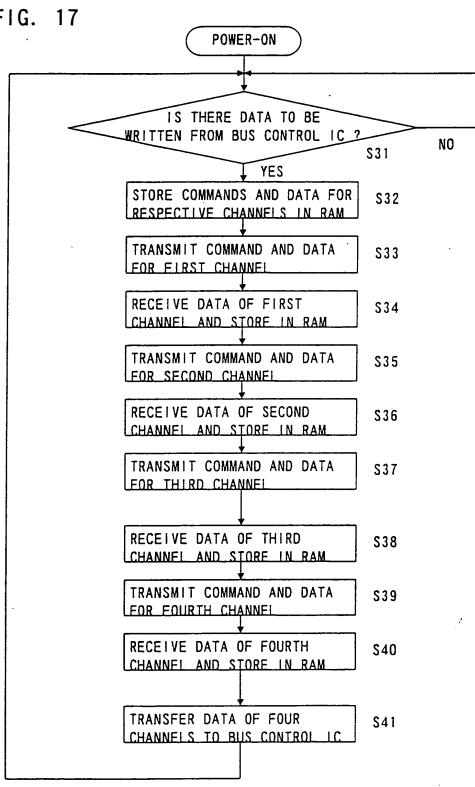
FIG. 16

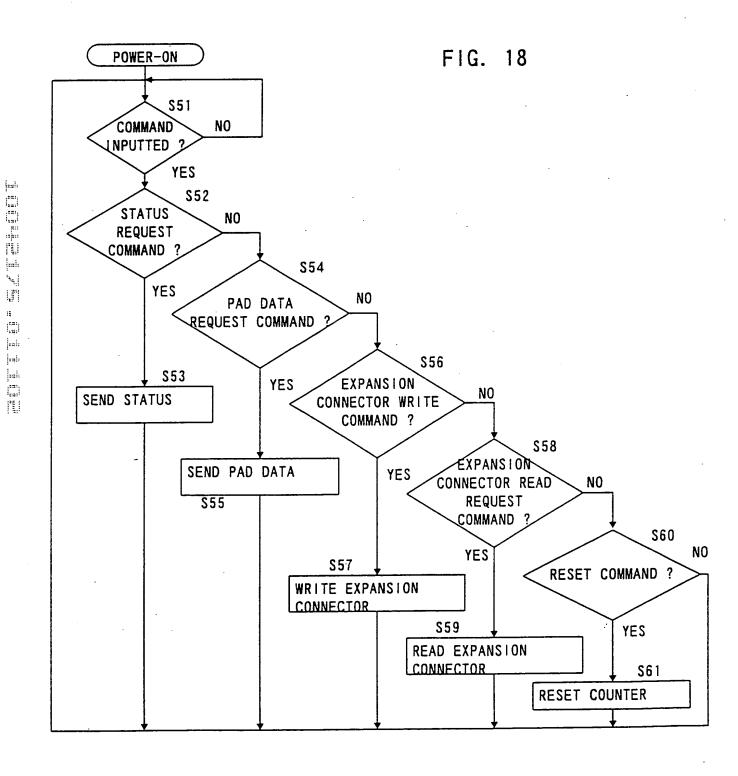


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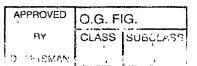


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FIG. 19

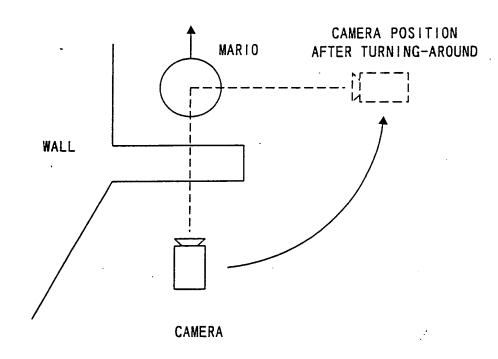
COMMAND 255: CONTROLLER RESET

RECEPTION: 1 BYTE TRANSMISSION: 3 BYTES d0d3 d2d 1 d7 d6 d5d4 RECP-_____COMMAND 255 — 1 BYTE TION _____ TYPE L _____ 1 BYTE TRANS-____ TYPE H -2 BYTE MISSLON — STATUS -3 BYTE



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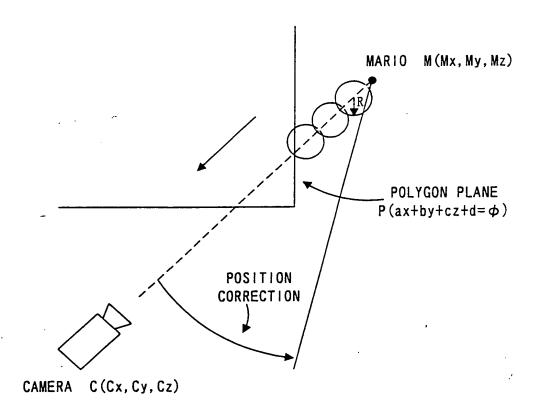
FIG. 20



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FIG. 21



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FIG. 22

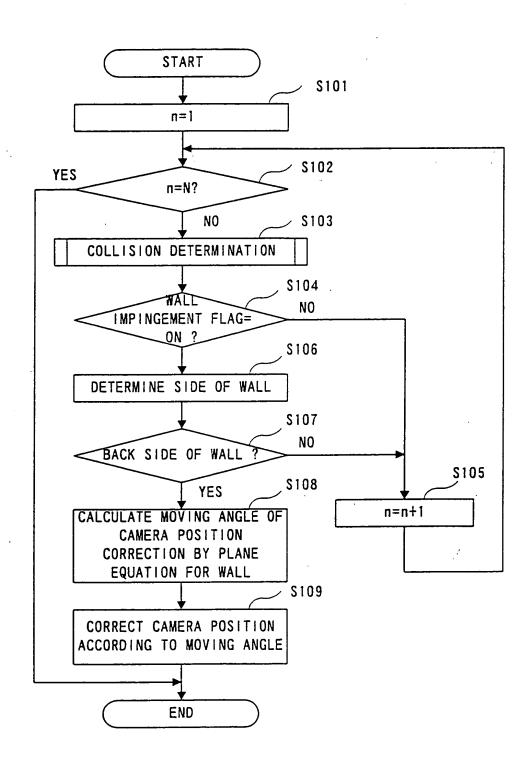
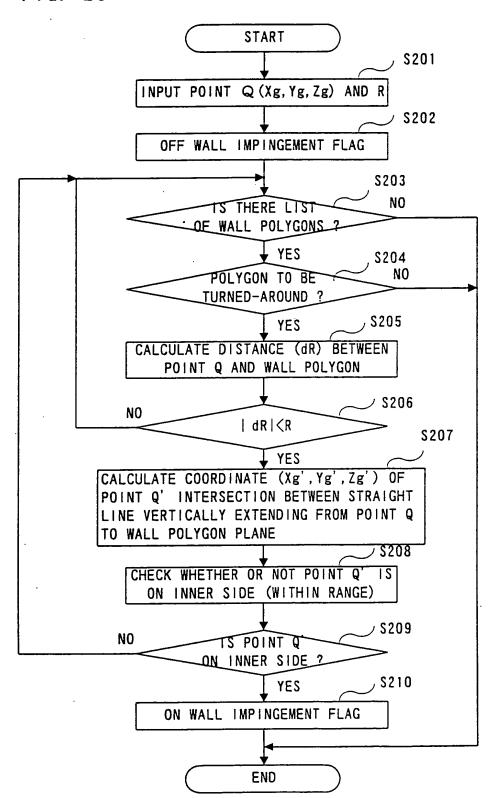
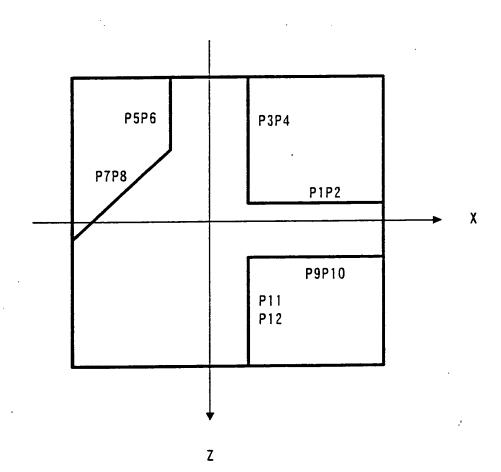


FIG. 23



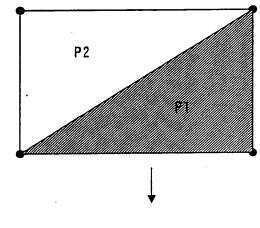
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BY	CLASS	SUBCLASS		
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FIG. 24



APPROVED	O.G. FIG.		
BY	CLASS	SUBCLASS	
DRAFTSMAN		1.	

FIG. 25



TRIANGLE POLYGON

APPROVED	O.G. FIG.			
BY	CLASS	SUBCLASS		
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FIG. 26

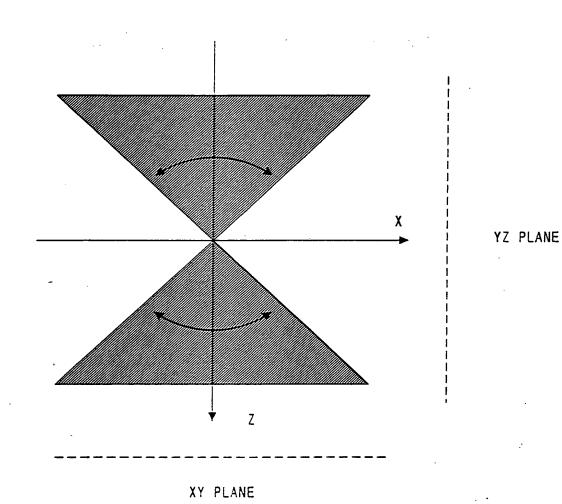
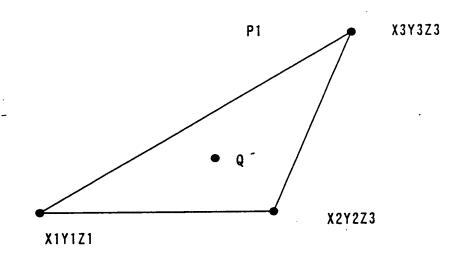


FIG. 27



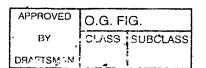
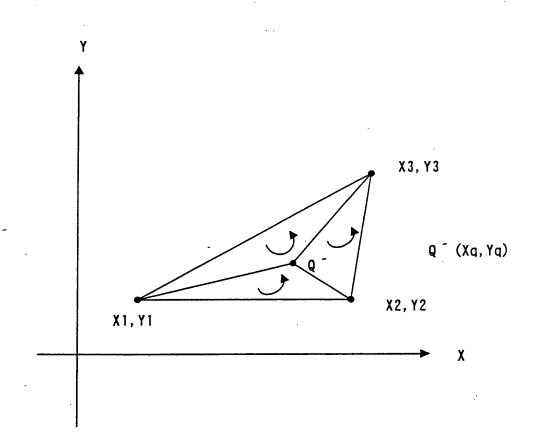


FIG. 28



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BY *	CLASS SUBCLASS	•
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FIG. 29

